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SPECIFICATION

Product Model: PV03502LZR40D

DESIGNED	CHECKED	Approved
研发部	研发部	研发部
2019.02.18	2019.02.18	2019.02.18
Aleck	Hones	Mike

Approval by Customer:

Ok

NG, Problem survey

Approved By_____

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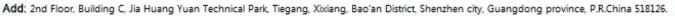
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Revision Record

REV NO.	REV DATE	CONTENTS	Note
V0	2019.02.18	NEW ISSUE	

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1. Scope

This specification defines general provisions as well as inspection standards for TFT module supplied by Kingtech Group Co.,Ltd.

If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution.

2. General Information

TITEM	STANDARD VALUES	UNITS
LCD type	3.5"TFT	
Dot arrangement	480(RGB)×640	dots
Color filter array	RGB vertical stripe	
Display mode	Wide View+TF / Normally Black	-
Gray Scale Inversion Direction	80/80/80/80 deg(U/D/L/R @ C/R>10)	
Eyes Viewing Direction	ALL	
Driver IC	HX8363A	
Module size	63(W)×85(H)×2.9(T)	mm
Active area	5357W)×71.42(H)	mm
Dot pitch	0.116(W)×0.116(H)	mm
Interface	3-wire SPI + RGB interface	
Operating temperature	-20 ~ +70	°C
Storage temperature	-30 ~ +80	°C
Back Light	8 White LEDS	
Weight	TBD	g

Note 1: Viewing direction for best image quality is different from TFT definition, there is a 180 degree shift.

Note 2: Requirements on Environmental Protection: RoHS

Note 3: LCM weight tolerance: +/- 5%

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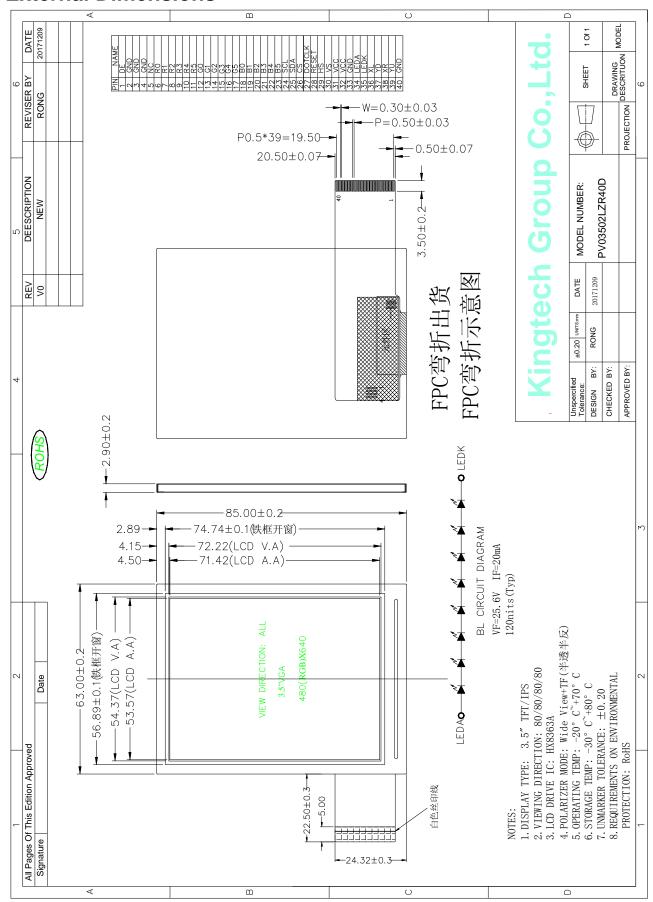
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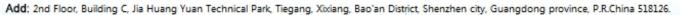
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3. External Dimensions



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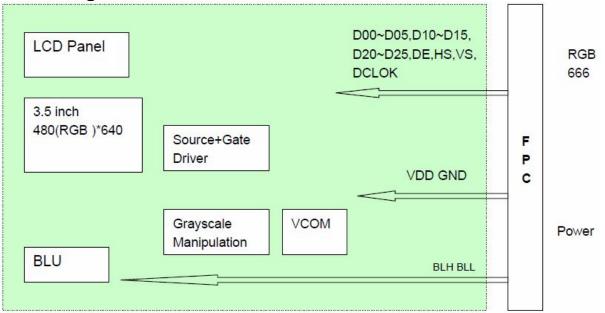
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4. Interface Description

4. IIIICII	ace Desci	iption
PIN	PIN NAME	DESCRIPTION
1	ENABLE	Data enable signal in RGB I/F mode
2-4	GND	Power ground
5	NC	NC
6-11	R0-R5	RED DATE
12-17	G0-G5	GREEN DATE
18-23	B0-B5	BIUE DATE
24	SCL	Serial clock input in SPI interface
25	SDI	Serial input signal in SPI I/F.
26	CS	Chip Select pin for Serial Mode Interface
27	DOTCLK	Pixel clock signal in RGB I/F.
28	RESET	Reset pin
29	HS	Horizontal sync signal in RGB I/F.
30	VS	Vertical sync signal in RGB I/F.
31-32	VCI	A supply voltage to the analog circuit.
33	GND	Power ground
34	LEDA	LED backlight (Anode).
35	LEDK	LED backlight (Cathode).
36	XL	RTP PIN
36	YD	RTP PIN
36	XR	RTP PIN
36	YU	RTP PIN
40	GND	Power ground

Block Diagram



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5. Absolute Maximum Ratings

Driving TFT LCD Panel

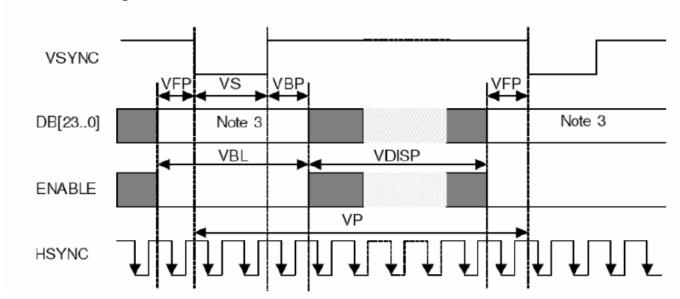
Item	Symbol	MIN	MAX	Unit	Remark
Supply Voltage	V_{DD}	-0.3	4.6	V	
Operating Temperature	Topr	-20	70	°C	
Storage Temperature	Tstg	-30	80	$^{\circ}$	

6. DC Characteristics

Item		Symbol	MIN	TYP	MAX	Unit	Remark
Cumpler V	altana	Vcc	2.5	3.0	3.3	V	
Supply V	onage	IOVCC	1.65	3.0	3.3		
Input Signal	Low Leve	V_{IL}	0		0.3x IOVCC	V	
Voltage	High Level	VIH	0.7x IOVCC	-	IOVCC	v	
Output Signal	Low Leve	Vol	0	-	0.2*IOVCC	V	
Voltage	High Level	Voн	0.8*IOVCC	-	IOVCC	V	

7. Timing Characteristics 7.1 RGB Interface Timing

Vertical Timings for RGB I/F



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(Resolution=480x854, VSSA=VSSD=0V, VDD1=1.65V to 3.3V, VDD2=2.5 to 3.3V, VDD3=2.5 to 3.3V, $T_A = -30$ to $70 \,^{\circ}$ C)

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Vertical cycle	VP		860	٠	864	Line
Vertical low pulse width	VS	•	2	1	4	Line
Vertical front porch	VFP	-	2	•	4	Line
Vertical back porch	VBP	-	2	-	4	Line
Vertical data start point	-	VS+VBP	4	-	8	Line
Vertical blanking period	VBL	VS+VBP+VFP	6		10	Line
Vertical active area	-	VDISP	-	854	-	Line
Vertical Refresh rate	VRR	-	50	-	70	Hz

Note: (1) Signal rise and fall times are equal to or less than 20 ns.

- (2) Input signals are measured by 0.30 x VDD1 for low state and 0.70 x VDD1 for high state.
- (3) Data lines can be set to "High" or "Low" during blanking time Don't care.
- (4) VRR must keep from 50Hz to 70Hz when adjust other items.

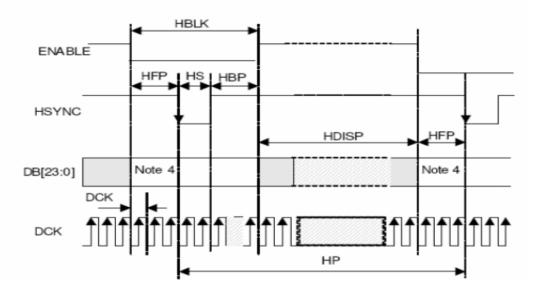
(Resolution=480x800, VSSA=VSSD=0V, VDD1=1.65V to 3.3V, VDD2=2.5 to 3.3V, VDD3=2.5 to 3.3V, $T_A = -30$ to 70 °C)

ltem	Symbol	Condition	Min.	Тур.	Max.	Unit
Vertical cycle	VP	-	806	-	810	Line
Vertical low pulse width	VS	-	2	-	4	Line
Vertical front porch	VFP	-	2	-	4	Line
Vertical back porch	VBP	-	2	-	4	Line
Vertical data start point	-	VS+VBP	4	-	8	Line
Vertical blanking period	VBL	VS+VBP+VFP	6	-	10	Line
Vertical active area	-	VDISP	-	800	-	Line
Vertical Refresh rate	VRR	-	50	-	70	Hz

Note: (1) Signal rise and fall times are equal to or less than 20 ns.

- (2) Input signals are measured by 0.30 x VDD1 for low state and 0.70 x VDD1 for high state.
- (3) Data lines can be set to "High" or "Low" during blanking time Don't care.
- (4) VRR must keep from 50Hz to 70Hz when adjust other items.

Horizontal Timings for RGB I/F



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(Resolution=480x854, VSSA=VSSD=0V, VDD1=1.65V to 3.3V, VDD2=2.5 to 3.3V, VDD3=2.5 to 3.3V, $T_A = -30$ to 70° C)

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
HS cycle	HP	Note ⁽³⁾	504	-	568	DCK
HS low pulse width	HS	-	5	-	78	DCK
Horizontal back porch	HBP	-	5	-	78	DCK
Horizontal front porch	HFP	-	5	-	78	DCK
Horizontal data start point	-	HS+HBP	19	-	83	DCK
Horizontal blanking period	HBLK	HS+HBP+HFP	24	-	88	DCK
Horizontal active area	HDISP	-	1	480	-	DCK
Pixel clock frequency	DCK	VRR = Min. 50Hz	21.6	-	34.3	MHz
When RGB I/F is running	DON	- Max. 70Hz	29.1	-	46.2	ns

Note: (1) Signal rise and fall times are equal to or less than 20 ns.

- (2) Input signals are measured by 0.30 x VDD1 for low state and 0.70 x VDD1 for high state.
- (3) HP is multiples of eight DCK.
- (4)Data lines can be set to "High" or "Low" during blanking time Don't care.
- (5) VRR must keep from 50Hz to 70Hz when adjust other items.

(Resolution=480x800, VSSA=VSSD=0V, VDD1=1.65V to 3.3V, VDD2=2.5 to 3.3V, VDD3=2.5 to 3.3V, $T_A = -30$ to 70 °C)

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
HS cycle	HP	Note ⁽³⁾	504	-	568	DCK
HS low pulse width	HS		5	-	78	DCK
Horizontal back porch	HBP		5	-	78	DCK
Horizontal front porch	HFP	-	5	-	78	DCK
Horizontal data start point	-	HS+HBP	19	-	83	DCK
Horizontal blanking period	HBLK	HS+HBP+HFP	24	-	88	DCK
Horizontal active area	HDISP		-	480	-	DCK
Pixel clock frequency	DCK	VRR = Min. 50Hz	20.3	-	32.2	MHz
When RGB I/F is running	DON	- Max. 70Hz	31	-	49.2	ns

Note: (1) Signal rise and fall times are equal to or less than 20 ns.

- (2) Input signals are measured by 0.30 x VDD1 for low state and 0.70 x VDD1 for high state.
- (3) HP is multiples of eight DCK.
- (4)Data lines can be set to "High" or "Low" during blanking time Don't care.
- (5) VRR must keep from 50Hz to 70Hz when adjust other items.

8. Backlight Characteristic

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Forward Current	I_{F}	20. 7	20		mA	
Forward Voltage	VF	24	25.6		V	
Backlight Power consumption	W _{BL}	1-	TBD		W	

Note 1: Each LED: IF =20 mA, VF =3.2V.

Note 2: Optical performance should be evaluated at Ta=25 ℃ only.

Note 3: If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.

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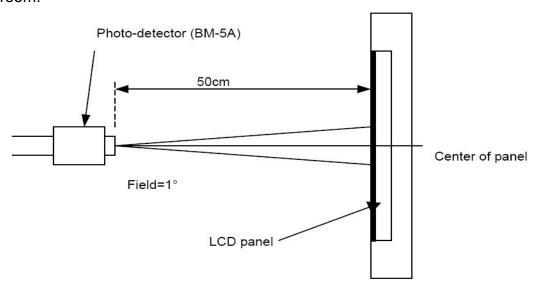
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9. Optical Characteristics

Item	Conditions		Min.	Тур.	Max.	Unit	Note	
Viewing Angle (CR>10)	Horizontal	θL	-	80	-	degree		
		θR	-	80	-		(1),(2),(6)	
	Vertical	θт	ı	80	-			
		θв	-	80	-			
Center Luminance of White	Lc		100	120	-	cd/m ²		
Contrast Ratio	Center		-	300	-	-	(1),(3),(6)	
Pospopo Timo	Rising			25	35	ms	(1),(4),(6)	
Response Time	Falling							
	Red x		Typ	-	Typ. +0.05	-	(1), (6)	
	Red y			-		-		
CF Color Chromaticity (CIE1931)	Green x			-		-		
	Green y			-		-		
	Blue x			-		-		
	Blue y			_		-		
	White x			-		-		
	White y			-		-		

Note (1) Measurement Setup: The LCD module should be stabilized at given temp. 25°C for 15 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 15 minutes in a windless room.



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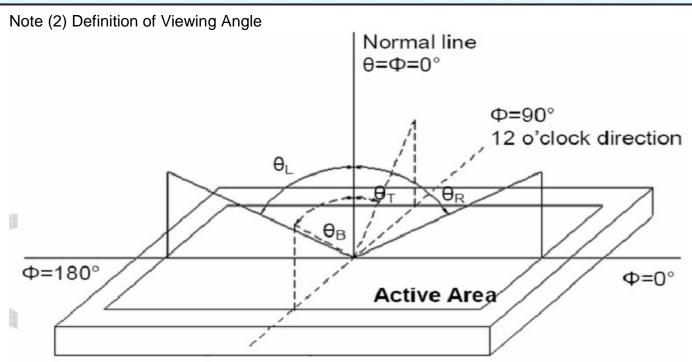
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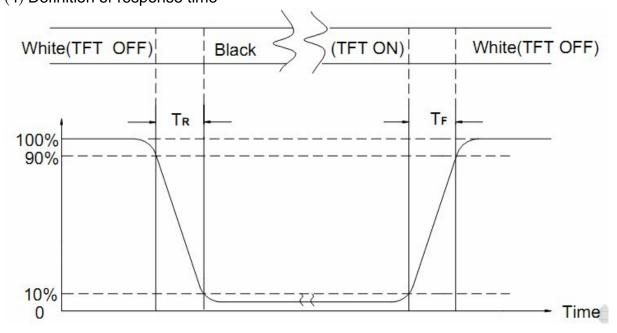


Note (3) Definition of Contrast Ratio (CR)

The contrast ratio can be calculated by the following expression Contrast Ratio (CR) = L63 / L0

 $\Phi = 270^{\circ}$

L63: Luminance of gray level 63, L0: Luminance of gray level 0 Note (4) Definition of response time



Note (5) Definition of Transmittance (Module is without signal input)

Transmittance = Center Luminance of LCD / Center Luminance of Back Light x 100%

Note (6) Definition of color chromaticity (CIE1931)

Color coordinates measured at the center point of LCD

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10. Reliability Test Conditions and Methods

NO.	TEST ITEMS	TEST CONDITION			
1	High Temperature Storage	Keep in 80°C ±5°C 240 hrs Surrounding temperature, then storage at normal condition 4hrs.			
2	Low Temperature Storage	Keep in -30°C ±5°C 240 hrs Surrounding temperature, then storage at normal condition 4hrs.			
3	High Temperature / High Humidity Storage Test	Keep in 50 °C / 90% R.H duration for 240 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)			
4	Temperature Cycling Storage Test	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$			
(5)	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance : 15°C~35°C 2. Humidity relative : 30%~60% 3. Energy Storage Capacitance(Cs + Cd) : 150pF±10% 4. Discharge Resistance(Rd) : 330Ω±10% 5. Discharge, mode of operation : Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication : ±5%)			
6	Vibration Test (Packaged)	 Sine wave 10~55 Hz frequency (1 min/sweep) The amplitude of vibration :1.5 mm Each direction (X、Y、Z) duration for 2 Hrs 			
7	Drop Test (Packaged)	Packing Weight (Kg) Drop Height (cm) 0 ~ 45.4 122 45.4 ~ 90.8 76 90.8 ~ 454 61 Over 454 46 Drop Direction: **1 corner / 3 edges / 6 sides each 1 time			

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11. Inspection Standard

11.1. QUALITY:

THE QUALITY OF GOODS SUPPLIED TO PURCHASER SHALL COME UP TO THE FOLLOWING STANDARD.

11.1.1. INSPECTIONTOOLS AND INSTRUMENTS

Vernier calipers, film scales, multimeter, magnifying eyepiece, ND5%, luminance meter and so on.

11.1.2. THE METHOD OF PRESERVING GOODS

AFTER DELIVERY OF GOODS FROM KINGTECHO PURCHASER. PURCHASER SHALL CONTROL THE LCM AT -10 TO 40 ,AND IT MIGHT BE DESIRABLE TO KEEP AT THE NORMAL ROOM TEMPERATURE AND HUMIDITY UNTIL INCOMING INSPECTION OR THROWING INTO PROCESS LINE.

11.1.3. INCOMING INSPECTION

(A) THE METHOD OF INSPECTION IF PURCHASER MAKE AN INCOMING INSPECTION, A SAMPLING PLAN SHALL BE APPLIED ON THE CONDITION THAT QUALITY OF ONE DELIVERY SHALL BE REGARDED AS ONE LOT.

(B) THE STANDARD OF QUALITY

ISO-2859-1 (SAME AS MIL-STD-105E) .LEVEL: II

	, ,
CLASS	AQL(%)
CRITICAL	0.4 %
MAJOR	0.65 %
MINOR	1.5 %

EVERY ITEM SHALL BE INSPECTED ACCORDING TO THE CLASS.

(C) MEASURE

IF AS THE RESULT OF ABOVE RECEIVING INSPECTION, A LOT OUT IS DISCOVERED. PURCHASER SHALL BE INFORM SELLER OF IT WITHIN SEVEN DAYS. BUT FIRST SHIPMENT WITHIN FOURTEEN DAYS.

11.1.4. WARRANTY POLICY

KINGTECHWILL PROVIDE ONE-YEAR WARRANTY FOR THE PRODUCTS ONLY IF UNDER SPECIFICATION OPERATING CONDITIONS. KINGTECHWILL REPLACE NEW PRODUCTS FOR THESE DEFECT PRODUCTS WHICH UNDER WARRANTY PERIOD AND BELONG TO THE RESPONSIBILITY OF KINGTECH.

11.2. CHECKING CONDITION

- 11.2.1. CHECKING DIRECTION SHALL BE IN THE 45 DEGREE AREA TO FACE THE SAMPLE.
- 11.2.2. CHECKER SHALL SEE OVER 300±25 mm. WITH BARE EYES FAR FROM SAMPLE
- **11.2.3.** Ambient Illumination:

0 ~30 Lux for functional inspection 500 ~ 1200 Lux for external appearance inspection.

11.2.4. TEST AREA:



rope electrostatic ring and static finger cover (both hands except small fingers must be worn)

11.2.6. The inspector may make a visual inspection or a comparative examination with a film ruler and a magnifying eyepiece. Individual defects shall be determined according to the limited

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samples.

- **11.2.7.** Functional testing uses electrical testing fixtures or test fixtures required by customers.
- **11.2.8.** the ion fan should be used when testing.

11.2.9. the principle of judgment

11.3.1 If the defect outside the visual area does not affect the assembly and display, it will be judged as a good product.

11.3.2 Poor definition

Pixel:

A combination of three sub-pixels (Red + Green + Blue).



Dot:

Any of the sub-pixels (Red or Green or Blue).







Bright and dark dots:

A point pixel (sub-pixel: R, G, B pixels) is lit or turned off during the display function test.

Highlights:

Usually considered to be shown on a black screen.

Dark spots:

They are generally considered to be shown on R, G, B solid colors or white images.

Neighborhood:

Two or three adjacent point pixels (dot: sub-pixel) connected together (R, G or G, B or B, R or RGB).

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11.3. INSPECTION PLAN:

11.3. INSPEC	TION PLAN:		
CLASS	ITEM	JUDGEMENT	CLASS
	1. OUTSIDE AND INSIDE PACKAGE	"MODEL NO.", "LOT NO." AND "QUANTITY"	Minor
PACKING &		SHOULD INDICATE ON THE PACKAGE.	
INDICATE	2. MODEL MIXED AND QUANTITY	OTHER MODEL MIXEDREJECTED	Critical
		QUANTITY SHORT OR OVERREJECTED	
	3. PRODUCT INDICATION	"MODEL NO." SHOULD INDICATE ON	Major
		THE PRODUCT	,
	4. DIMENSION,	ACCORDING TO SPECIFICATION OR	
ASSEMBLY	LCD GLASS SCRATCH	DRAWING.	Major
	AND SCRIBE DEFECT.		,
	5. VIEWING AREA	POLARIZER EDGE OR LCD'S SEALING LINE	Minor
		IS VISABLE IN THE VIEWING AREA	
		REJECTED	
	6. BLEMISH - BLACK SPOT -	ACCORDING TO STANDARD OF VISUAL	Minor
	WHITE SPOT IN THE LCD	INSPECTION(INSIDE VIEWING AREA)	
	AND LCD GLASS CRACKS	INST ECTION(INSIDE VIEWING AREA)	
	7. BLEMISH - BLACK SPOT	ACCORDING TO STANDARD OF VISUAL	Minor
APPEARANCE	WHITE SPOT AND SCRATCH		WIIIIOI
ALLEANANOL	ON THE POLARIZER	INSPECTION(INSIDE VIEWING AREA)	
	8. BUBBLE IN POLARIZER	ACCORDING TO STANDARD OF VISUAL	Minor
	6. BOBBLE IN FOLARIZER		WIIIIOI
	9. LCD'S RAINBOW COLOR	INSPECTION(INSIDE VIEWING AREA) STRONG DEVIATION COLOR (OR NEWTON	
	5. LCD 3 RAINBOW COLOR		Minor
		RING) OF LCDREJECTED.	MILIOI
		OR ACCORDING TO LIMITED SAMPLE	
		(IF NEEDED, AND INSIDE VIEWING AREA)	0.77
	10. ELECTRICAL AND OPTICAL	ACCORDING TO SPECIFICATION OR	Critical
	CHARACTERISTICS	DRAWING . (INSIDE VIEWING AREA)	
	(CONTRAST VOP		
EL EGTDION.	CHROMATICITY ETC)		
ELECTRICAL	11.MISSING LINE	MISSING DOT LINE CHARACTER	Critical
		REJECTED	
	12.SHORT CIRCUIT	NO DISPLAY - WRONG PATTERN	Critical
	WRONG PATTERN DISPLAY	DISPLAY · CURRENT CONSUMPTION	
		OUT OF SPECIFICATION REJECTED	
	13. DOT DEFECT (FOR COLOR AND TFT) ACCORDING TO STANDARD OF VISUAL	Minor
		INSPECTION	

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NO.	CLASS	ITEM	JUDGEMENT			
			(A) ROUND TYPE: unit : mm.			
		DIAMETER (mm.) ACCEPTA	BLE Q'TY			
		Φ ≤ 0.15 Dist	tance≥1mm			
	BLACK AND WHITE SPOT	$0.15 < \Phi \le 0.4$ 3 (Dist	tance>15mm)			
		0.4 < Ф	0			
11 4 1	11.4.1 MINOR	FOREIGN MATERIEL DUST IN THE CELL BLEMISH SCRATCH	NOTE: Φ =(LENGTH+WIDTH)/2			
11.4.1			(B) LINEAR TYPE:	unit : mm.		
			LENGTH WIDTH ACC	CEPTABLE Q'TY		
			W ≦0.03	Distance≥1mm		
			L ≦ 4.0 0.03 < W ≦0.05	3 (Distance>15mm)		
			0.05 < W FOI	LLOW ROUND TYPE		
\vdash						
			DIAMETER ACCEPT	unit : mm. TABLE Q'TY		
		BUBBLE IN POLARIZER		tance≥1mm		
11.4.2	MINOR		V = 1.2	tance>15mm)		
			0.3 < Φ	0		
			0.0			
		Dot Defect	Items ACC	C. Q'TY		
			Bright dot N≤2 (Di	istance≧15mm)		
			Dark dot N≤3 (Di	istance>15mm)		
			Pixel Define : Pixel	•l		
			R G B			
			R G B			
11 / 3	MINOR		4 Dat No. Dat No. Date			
11.4.3	WIIIVOIX		◆ Dot → ◆ Dot → Dot			
			1/2 of whole dot is regarded as one defective dot. Definittion:<1/2dot and visible by 5 % ND filter N ≤ 5			
			Note 2: Bright dot: Dots appear bright and unchanged in size			
			in which LCD panel is displaying under black pattern.			
			Note 3: Dark dot: Dots appear dark and un	·		
			which LCD panel is displaying under pure red, green			
			,blue pattern.	,, g		
\vdash			•	gray or kides		
11 4 4	MINIOP	Mura	Not visible thriugh 5% ND filter in 50% gray or judge			
11.4.4 MINOR			by limit sample if necessary			

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NO.	CLASS	ITEM	JUDGEMEN	Т
11.4.4	MINOR	LCD GLASS CHIPPING	S	Y > S Reject
11.4.5	MINOR	LCD GLASS CHIPPING	SX	X or Y > S Reject
11.4.6	MAJOR	LCD GLASS GLASS CRACK	Y	Y > (1/2) T Reject
11.4.7	MAJOR	LCD GLASS SCRIBE DEFECT	A + B	1. a> L/3 , A>1.5mm. Reject 2. B: ACCORDING TO DIMENSION
11.4.8	MINOR	LCD GLASS CHIPPING (ON THE TERMINAL AREA)	T	Φ = (x+y)/2 > 2.5 mm Reject
11.4.9	MINOR	LCD GLASS CHIPPING (ON THE TERMINAL SURFACE)	TZX	Y > (1/3) T Reject
11.4.10	MINOR	LCD GLASS CHIPPING	T Z	Y > T Reject

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12. Handling Precautions

12.1 Mounting method

The LCD panel of KINGTECHTFT module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (Cl) , Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

12.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to POWER or GROUND, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

12.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

12.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the maximum operating temperature, 50%Rh or less is required.

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12.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
 [It is recommended to store them as they have been contained in the inner container at the time of delivery from us

12.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

13. Precaution for Use

13.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

13.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification
- When a new problem is arisen which is not specified in this specifications
- When an inspection specifications change or operating condition change in customer is reported to KINGTECHIFT, and some problem is arisen in this specification due to the change
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

14. Packing Method

TBD

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