2nd Floor, Building C, Jia Huang Yuan Technical Park, Tiegang, Xixiang, Bao'an District, Shenzhen city, Guangdong province, P.R.China 518126.





# **SPECIFICATION**

Product Model: PV10107LZR40E

DESIGNED	CHECKED	Approved
研发部	研发部	研发部
2019.05.07	2019.05.07	2019.05.07
Aleck	Hones	Mike

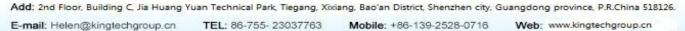
# **Approval by Customer:**

Ok

NG, Problem survey

Approved By\_\_\_\_\_

Rev.V0 1/21





# **Revision Record**

V0     2018.01.23     NEW ISSUE       V1     2019.05.07     修改可靠性参数	
V1 2019.05.07 修改可靠性参数	

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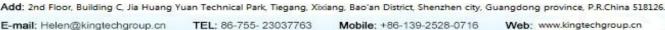


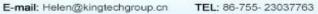


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Mobile: +86-139-2528-0716



# 1. Scope

This specification defines general provisions as well as inspection standards for TFT module supplied by Kingtech.

If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution

# 2. General Information

## **LCM**

ITEM	STANDARD VALUES	UNITS
LCD type	10.1"TFT	
Dot arrangement	1280×3(RGB)×800	dots
Color filter array	RGB vertical stripe	
Display mode	Normally Black	-
Viewing Direction	85/85/85	
Module size	229.46(W)×149.10(H)×2.8(T)	mm
Active area	216.96(W)×135.60(H)	mm
Dot pitch	0.1695(W)×0.1695(H)	mm
Interface	LVDS Interface	
Operating temperature	-10 ~ +70	°C
Storage temperature	-20 ~ +70	°C
Weight	TBD	g

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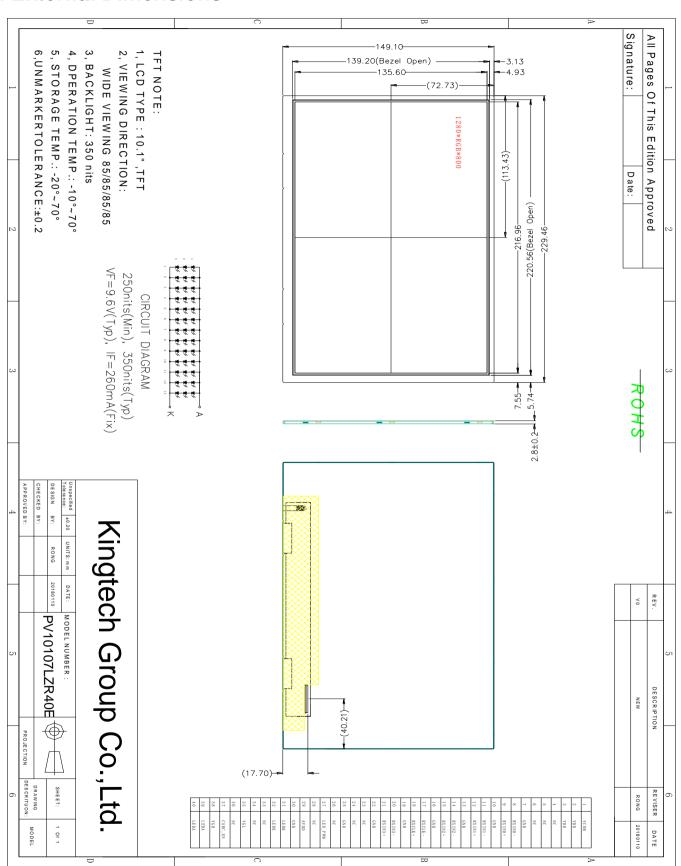
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Web: www.kingtechgroup.cn

## 3. External Dimensions



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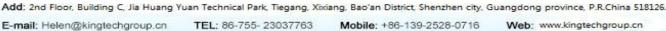
E-mail: Helen@kingtechgroup.cn TEL: 86-755- 23037763 Mobile: +86-139-2528-0716



4. Interface Description

PIN   PIN NAME	<u>4. milen</u>	Interface Description							
2	PIN	PIN NAME	DESCRIPTION	Remark					
No	1	VCOM	Common Voltage						
A	2	VDD	Dower Cumhi						
S	3	VDD	Power Supply						
6         NC           7         GND         Ground           8         Rxin0-         -LVDS Differential Data Input         R0-R5,G0           9         Rxin0+         +LVDS Differential Data Input         R0-R5,G0           10         GND         Ground         G1-G5,B0,B1           11         Rxin1-         -LVDS Differential Data Input         G1-G5,B0,B1           12         Rxin1+         +LVDS Differential Data Input         B2-B5,HS,           13         GND         Ground         B2-B5,HS,           14         Rxin2-         -LVDS Differential Data Input         B2-B5,HS,           15         Rxin2+         +LVDS Differential Clock Input         LVDS CLK           16         GND         Ground         Ground           17         RxCLK-         +LVDS Differential Clock Input         LVDS CLK           18         RxCLK+         +LVDS Differential Data Input         R6,R7,G6,G7,           20         Rxin3-         +LVDS Differential Data Input         R6,B7           21         Rxin3-         +LVDS Differential Data Input         R6,B7           22         GND         Ground         G0           23         NC         No connection         No conlectio	4	NC							
7         GND         Ground           8         Rxin0-         LVDS Differential Data Input         R0-R5,G0           9         Rxin0+         +LVDS Differential Data Input         R0-R5,G0           10         GND         Ground         G1-G5,B0, B1           11         Rxin1+         +LVDS Differential Data Input         G1-G5,B0, B1           12         Rxin1+         +LVDS Differential Data Input         B2-B5,HS,           13         GND         Ground         B2-B5,HS,           14         Rxin2-         +LVDS Differential Data Input         B2-B5,HS,           15         Rxin2+         +LVDS Differential Clock Input         LVDS CLK           16         GND         Ground         GND           17         RxCLK-         +LVDS Differential Clock Input         LVDS CLK           18         RxCLK+         +LVDS Differential Data Input         R6,R7,G6,G7,B6,G7,B6,G7,B6,G7,B6,G7           19         GND         Ground         Ground         R6,B7           21         Rxin3+         +LVDS Differential Data Input         R6,B7           22         GND         Ground         Ground           23         NC         No connection         No connection           <	5	NC	No connection						
8         Rxin0-         -LVDS Differential Data Input         R0-R5,G0           9         Rxin0+         +LVDS Differential Data Input         R0-R5,G0           10         GND         Ground         G1-G5,B0, B1           11         Rxin1-         +LVDS Differential Data Input         G1-G5,B0, B1           12         Rxin1-         +LVDS Differential Data Input         B2-B5,HS,           13         GND         Ground         B2-B5,HS,           14         Rxin2-         +LVDS Differential Data Input         B2-B5,HS,           15         Rxin2+         +LVDS Differential Clock Input         LVDS CLK           16         GND         Ground         Ground           17         RXCLK-         +LVDS Differential Clock Input         LVDS CLK           18         RXCLK-         +LVDS Differential Data Input         R6,R7,G6,G7,B6,G7,B6,B7           19         GND         Ground         Ground         R6,R7,G6,G7,B6,B7           21         Rxin3-         +LVDS Differential Data Input         R6,R7,G6,G7,B6,B7           21         Rxin3-         +LVDS Differential Data Input         R6,R7,G6,G7,B6,B7           24         NC         No connection         No connection           25         GND <td>6</td> <td>NC</td> <td></td> <td></td>	6	NC							
9	7	GND	Ground						
9	8	Rxin0-	-LVDS Differential Data Input	D0 D5 C0					
11	9	Rxin0+	+LVDS Differential Data Input	R0~R5,G0					
12	10	GND	Ground						
12         RXin1+         +LVDS Differential Data Input           13         GND         Ground           14         Rxin2-         -LVDS Differential Data Input         B2~B5,HS, VS,DE           15         Rxin2+         +LVDS Differential Data Input         VS,DE           16         GND         Ground         LVDS CLK           17         RxCLK-         -LVDS Differential Clock Input         LVDS CLK           18         RxCLK+         +LVDS Differential Clock Input         R6,R7,G6,G7,G7,G7,G7,G7,G7           19         GND         Ground         R6,R7,G6,G7,G6,G7,G7,G7,G7           21         Rxin3+         +LVDS Differential Data Input         R6,B7           22         GND         Ground         GROUND           23         NC         No connection         No connection           24         NC         No connection         Note2           25         GND         Ground         Note2           28         NC         No connection         Note2           29         AVDD         Power for Analog Circuit         Note3           30         GND         Ground         Ground           31         LEDK         LED Cathode           32 </td <td>11</td> <td>Rxin1-</td> <td>-LVDS Differential Data Input</td> <td>C4</td>	11	Rxin1-	-LVDS Differential Data Input	C4					
14         Rxin2-         -LVDS Differential Data Input         B2~85,HS, VS,DE           15         Rxin2+         +LVDS Differential Data Input         VS,DE           16         GND         Ground         CyDS Differential Clock Input         LVDS CLK           17         RxCLK-         -LVDS Differential Clock Input         LVDS CLK           19         GND         Ground         Ground         R6,R7,G6,G7,G7,G6,G7,G7,G7,G7,G7,G7,G7,G7,G7,G7,G7,G7,G7,	12	Rxin1+	+LVDS Differential Data Input	G1~G5,B0, B1					
15	13	GND	Ground						
15	14	Rxin2-	-LVDS Differential Data Input	B2~B5,HS,					
17         RxCLK-         -LVDS Differential Clock Input         LVDS CLK           18         RxCLK+         +LVDS Differential Clock Input         LVDS CLK           19         GND         Ground         R6,R7,G6,G7,B6,G7,B6,B7           20         Rxin3-         +LVDS Differential Data Input         R6,R7,G6,G7,B6,B7           21         Rxin3+         +LVDS Differential Data Input         R6,B7           22         GND         Ground         G6,B7           23         NC         No connection         No connection           24         NC         No connection         No connection           25         GND         Ground         Ground         Note2           26         NC         No connection         Note2           27         LED_PWM         CABC controller signal output for backlight         Note2           28         NC         No connection         Note2           30         GND         Ground         Ground         Ground           31         LEDK         LED Cathode         LED Cathode         LED Cathode           33         NC         No connection         No connection         No connection           35         VGL         Gate OFF Voltage <td>15</td> <td>Rxin2+</td> <td>+LVDS Differential Data Input</td> <td></td>	15	Rxin2+	+LVDS Differential Data Input						
18         RxCLK+         +LVDS Differential Clock Input         LVDS CLK           19         GND         Ground         R6,R7,G6,G7,B6,G7,B6,B7           20         Rxin3-         +LVDS Differential Data Input         R6,R7,G6,G7,B6,B7           21         Rxin3+         +LVDS Differential Data Input         B6,B7           22         GND         Ground         Ground           23         NC         No connection         No connection           24         NC         No connection         No connection           25         GND         Ground         Ground         Note2           26         NC         No connection         Note2           27         LED_PWM         CABC controller signal output for backlight         Note2           28         NC         No connection         Note2           30         GND         Ground         Ground         Ground           31         LEDK         LED Cathode         LED Cathode           32         LEDK         LED Cathode         South Connection           34         NC         No connection         Note1           35         VGL         Gate OFF Voltage         Note1           36         NC<	16	GND	Ground						
18         RXCLK+         +LVDS Differential Clock Input           19         GND         Ground           20         Rxin3-         +LVDS Differential Data Input         R6,R7,G6,G7,B6,B7           21         Rxin3+         +LVDS Differential Data Input         B6,B7           22         GND         Ground         Ground           23         NC         No connection         No connection           24         NC         No connection         Note2           25         GND         Ground         Note2           26         NC         No connection         Note2           27         LED_PWM         CABC controller signal output for backlight         Note2           28         NC         No connection         Note2           30         GND         Ground         Ground         Note2           31         LEDK         LED Cathode         LED Cathode         Social Controller School           32         LEDK         LED Cathode         Note1         Note1           35         VGL         Gate OFF Voltage         Note1           36         NC         No connection         Note1           37         CABC_EN         CABC Enable Input	17	RxCLK-	-LVDS Differential Clock Input	17/00 01/4					
20         Rxin3-         -LVDS Differential Data Input         R6,R7,G6,G7,B6,B7           21         Rxin3+         +LVDS Differential Data Input         B6,B7           22         GND         Ground         Ground           23         NC         No connection         No connection           24         NC         No connection         No connection           26         NC         No connection         Note2           27         LED_PWM         CABC controller signal output for backlight         Note2           28         NC         No connection         Note2           29         AVDD         Power for Analog Circuit         Power for Analog Circuit           30         GND         Ground         Ground           31         LEDK         LED Cathode         LED Cathode           32         LEDK         LED Cathode         LED Cathode           34         NC         No connection         No connection           35         VGL         Gate OFF Voltage         No connection           37         CABC_EN         CABC Enable Input         Note1           38         VGH         Gate ON Voltage           39         LEDA         LED Anode </td <td>18</td> <td>RxCLK+</td> <td>+LVDS Differential Clock Input</td> <td>LVDS CLK</td>	18	RxCLK+	+LVDS Differential Clock Input	LVDS CLK					
21         Rxin3+         +LVDS Differential Data Input         B6,B7           22         GND         Ground           23         NC         No connection           24         NC         No connection           25         GND         Ground           26         NC         No connection           27         LED_PWM         CABC controller signal output for backlight         Note2           28         NC         No connection           29         AVDD         Power for Analog Circuit           30         GND         Ground           31         LEDK         LED Cathode           32         LEDK         LED Cathode           33         NC         No connection           34         NC         No connection           35         VGL         Gate OFF Voltage           36         NC         No connection           37         CABC_EN         CABC Enable Input         Note1           38         VGH         Gate ON Voltage           39         LEDA         LED Anode	19	GND	Ground						
21         Rxin3+         +LVDS Differential Data Input         B6,B7           22         GND         Ground           23         NC         No connection           24         NC         No connection           25         GND         Ground           26         NC         No connection           27         LED_PWM         CABC controller signal output for backlight         Note2           28         NC         No connection           29         AVDD         Power for Analog Circuit           30         GND         Ground           31         LEDK         LED Cathode           32         LEDK         LED Cathode           33         NC         No connection           34         NC         No connection           35         VGL         Gate OFF Voltage           36         NC         No connection           37         CABC_EN         CABC Enable Input         Note1           38         VGH         Gate ON Voltage           39         LEDA         LED Anode	20	Rxin3-	-LVDS Differential Data Input	R6,R7,G6,G7,					
23         NC         No connection           24         NC         No connection           25         GND         Ground           26         NC         No connection           27         LED_PWM         CABC controller signal output for backlight         Note2           28         NC         No connection           29         AVDD         Power for Analog Circuit           30         GND         Ground           31         LEDK         LED Cathode           32         LEDK         LED Cathode           33         NC         No connection           34         NC         No connection           35         VGL         Gate OFF Voltage           36         NC         No connection           37         CABC_EN         CABC Enable Input         Note1           38         VGH         Gate ON Voltage           39         LEDA         LED Anode	21	Rxin3+	+LVDS Differential Data Input						
24         NC         No connection           25         GND         Ground           26         NC         No connection           27         LED_PWM         CABC controller signal output for backlight         Note2           28         NC         No connection           29         AVDD         Power for Analog Circuit           30         GND         Ground           31         LEDK         LED Cathode           32         LEDK         LED Cathode           33         NC         No connection           34         NC         No connection           35         VGL         Gate OFF Voltage           36         NC         No connection           37         CABC_EN         CABC Enable Input         Note1           38         VGH         Gate ON Voltage           39         LEDA         LED Anode	22	GND	Ground						
24         NC           25         GND         Ground           26         NC         No connection           27         LED_PWM         CABC controller signal output for backlight         Note2           28         NC         No connection           29         AVDD         Power for Analog Circuit           30         GND         Ground           31         LEDK         LED Cathode           32         LEDK         LED Cathode           33         NC         No connection           34         NC         No connection           35         VGL         Gate OFF Voltage           36         NC         No connection           37         CABC_EN         CABC Enable Input         Note1           38         VGH         Gate ON Voltage           39         LEDA         LED Anode	23	NC	NIs connection						
26         NC         No connection           27         LED_PWM         CABC controller signal output for backlight         Note2           28         NC         No connection           29         AVDD         Power for Analog Circuit           30         GND         Ground           31         LEDK         LED Cathode           32         LEDK         LED Cathode           33         NC         No connection           34         NC         No connection           35         VGL         Gate OFF Voltage           36         NC         No connection           37         CABC_EN         CABC Enable Input         Note1           38         VGH         Gate ON Voltage           39         LEDA         LED Anode	24	NC	ino connection						
27 LED_PWM CABC controller signal output for backlight 28 NC No connection 29 AVDD Power for Analog Circuit 30 GND Ground 31 LEDK LED Cathode 32 LEDK LED Cathode 33 NC No connection 34 NC No connection 35 VGL Gate OFF Voltage 36 NC No connection 37 CABC_EN CABC Enable Input 38 VGH Gate ON Voltage 39 LEDA LED Anode	25	GND	Ground						
28         NC         No connection           29         AVDD         Power for Analog Circuit           30         GND         Ground           31         LEDK         LED Cathode           32         LEDK         LED Cathode           33         NC         No connection           34         NC         No connection           35         VGL         Gate OFF Voltage           36         NC         No connection           37         CABC_EN         CABC Enable Input         Note1           38         VGH         Gate ON Voltage           39         LEDA         LED Anode	26	NC	No connection						
29         AVDD         Power for Analog Circuit           30         GND         Ground           31         LEDK         LED Cathode           32         LEDK         LED Cathode           33         NC         No connection           34         NC         No connection           35         VGL         Gate OFF Voltage           36         NC         No connection           37         CABC_EN         CABC Enable Input         Note1           38         VGH         Gate ON Voltage           39         LEDA         LED Anode	27	LED_PWM	CABC controller signal output for backlight	Note2					
30         GND         Ground           31         LEDK         LED Cathode           32         LEDK         LED Cathode           33         NC         No connection           34         NC         No connection           35         VGL         Gate OFF Voltage           36         NC         No connection           37         CABC_EN         CABC Enable Input         Note1           38         VGH         Gate ON Voltage           39         LEDA         LED Anode	28	NC	No connection						
31         LEDK         LED Cathode           32         LEDK         LED Cathode           33         NC         No connection           34         NC         No connection           35         VGL         Gate OFF Voltage           36         NC         No connection           37         CABC_EN         CABC Enable Input         Note1           38         VGH         Gate ON Voltage           39         LEDA         LED Anode	29	AVDD	Power for Analog Circuit						
32         LEDK         LED Cathode           33         NC         No connection           34         NC         No connection           35         VGL         Gate OFF Voltage           36         NC         No connection           37         CABC_EN         CABC Enable Input         Note1           38         VGH         Gate ON Voltage           39         LEDA         LED Anode	30	GND	Ground						
33         NC         No connection           34         NC         No connection           35         VGL         Gate OFF Voltage           36         NC         No connection           37         CABC_EN         CABC Enable Input         Note1           38         VGH         Gate ON Voltage           39         LEDA         LED Anode	31	LEDK	LED Cathode						
34 NC No connection 35 VGL Gate OFF Voltage 36 NC No connection 37 CABC_EN CABC Enable Input Note1 38 VGH Gate ON Voltage 39 LEDA LED Anode	32	LEDK	LED Cathode						
34 NC 35 VGL Gate OFF Voltage 36 NC No connection 37 CABC_EN CABC Enable Input Note1 38 VGH Gate ON Voltage 39 LEDA LED Anode	33	NC	No connection						
36 NC No connection 37 CABC_EN CABC Enable Input Note1 38 VGH Gate ON Voltage 39 LEDA LED Anode	34	NC	INO CONNECTION						
37 CABC_EN CABC Enable Input Note1 38 VGH Gate ON Voltage 39 LEDA LED Anode	35	VGL	Gate OFF Voltage						
38 VGH Gate ON Voltage 39 LEDA LED Anode	36	NC	No connection						
39 LEDA LED Anode	37	CABC_EN	CABC Enable Input	Note1					
	38	VGH	Gate ON Voltage						
40 LEDA LED Anode	39	LEDA	LED Anode						
	40	LEDA	LED Anode						

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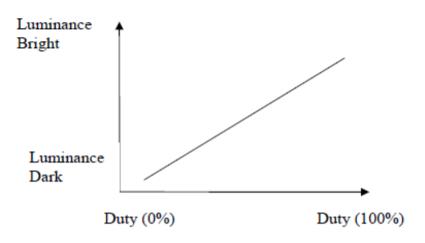




Note1: The setting of CABC function are as follows.

Pin Enable		Disable		
CABC_EN High Voltage		Low Voltage or open		

Note2: LED\_PWM is used to adjust backlight brightness.

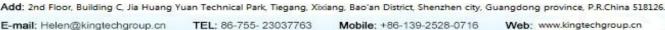


# 5. Absolute Maximum Ratings

(Note 1)

	(140	)			
Item	Symbol	Min.	Max.	Unit	Remark
Digital Supply Voltage	VDD	-0.3	3.9	V	
Analog Supply Voltage	AVDD	-0.3	14	V	
Gate On Voltage	Vgн	-0.3	42	V	
Gate Off Voltage	VgL	-19	0.3	V	
Gate On ~ Gate Off Voltage	Vgh-Vgl	12	40	V	
Operating Temperature	Тор	-10	70	°C	
Storage Temperature	Тѕт	-20	70	°C	
LED Reverse Voltage	VF	2.1	3.1	V	IF=20mA
LED Forward Current	lR	-	50	mA	VR=5V

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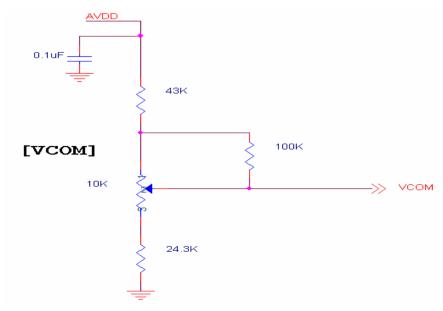
**Current Consumption** 

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
High Supply for Current	lgн	-	705	750	uA	VgH =22V
Low Supply for Current	lgL	-	705	750	uA	VGL = -7V
Logic Supply for Current	IVdd	-	95	120	mA	VDD =2.5V
Analog Supply for Current	IAvdd	-	45	70	mA	AVDD =8.2V

## 6. DC Characteristics

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Digital Supply Voltage	VDD	2.3	2.5	2.7	V	Note 1; Note 2
Analog Supply Voltage	AVDD	8.0	8.2	8.4	V	-
Gate On Voltage	VgH	21.7	22	22.3	V	Note 1
Gate Off Voltage	VGL	-7.3	-7.0	-6.7	V	Note 1
Input signal voltage	VCOM	2.7	3.0	3.3	V	Note 4
Input logic high voltage	VIH	0.8VDD	-	3.6	V	Note 3
Input logic low voltage	VIL	GND	-	0.2DVDD	V	Note 3

- Note 1: Be sure to apply VDD and VGL to the LCD first, AND THEN APPLY VGH
- Note 2: VDD setting should match the signals output voltage (refer to Note 3) of customer's System board
- Note 4: Typical VCOM is only a reference value, it must be optimized according to each LCM. Be sure to use VR.



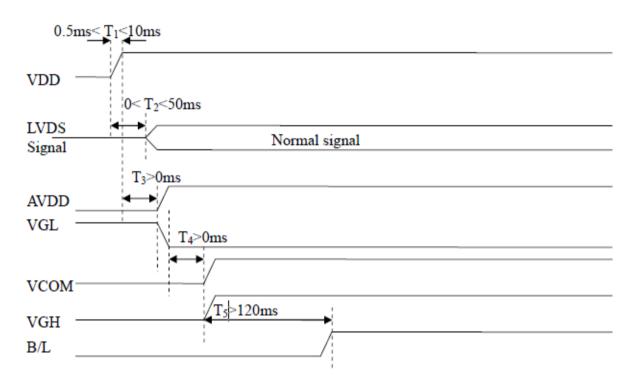
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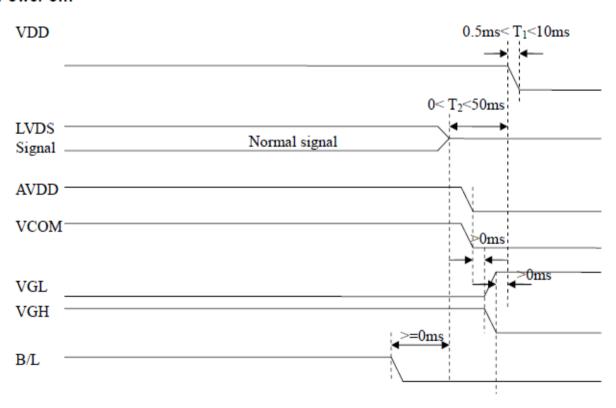
# 7. Timing Characteristics

# 7.1 Power Sequence

#### a. Power on:



#### b. Power off:



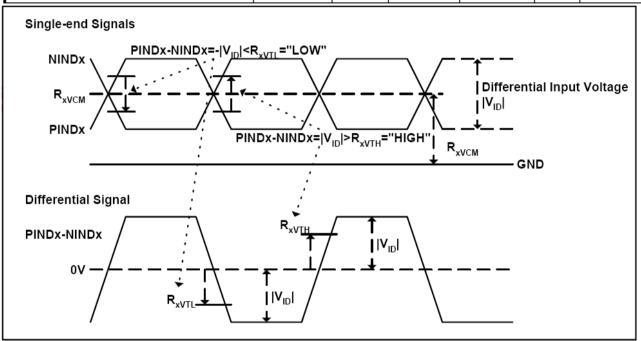
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# 7.2 LVDS Signal Timing Characteristics

## 7.2.1 AC Electrical Characteristics

Parameter	Symbol	Symbol Values				Remark
	- J	Min.	Тур.	Max.	Unit	
LVDS Differential input high Threshold voltage	R <sub>xVTH</sub>	-	-	+100	m∨	R <sub>XVCM</sub> =1.2V
LVDS Differential input low Threshold voltage	R <sub>xVTL</sub>	-100	-	-	m∨	TXXVCM-1.2V
LVDS Differential input common mode voltage	R <sub>xVCM</sub>	0.7	-	1.6	٧	
LVDS Differential voltage	V <sub>ID</sub>	200	-	600	m∨	



# 7.2.2 Timing Table

Item	Cymphal		Values	Unit	Demont	
item	Symbol	Min.	Тур.	Max.	Unit	Remark
Clock Frequency	1/Tc	(68.9)	71.1	(73.4)	MHz	Frame rate =60Hz
Horizontal display area	thD		1280		Tc	
HS period time	tн	(1410)	1440	(1470)	Tc	
HS Width +Back Porch +Front Porch	thw+ thbp +thpp	(60)	160	(190)	Tc	
Vertical display area	tvD		800		tн	
VS period time	tv	(815)	823	(833)	tн	
VS Width +Back Porch +Front Porch	tvw+ tvbP +tvFP	(15)	23	(33)	tн	

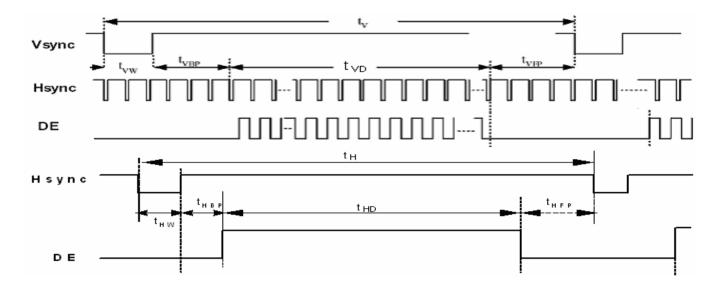
Rev.V0 10/21 E-mail: Helen@kingtechgroup.cn

TEL: 86-755-23037763

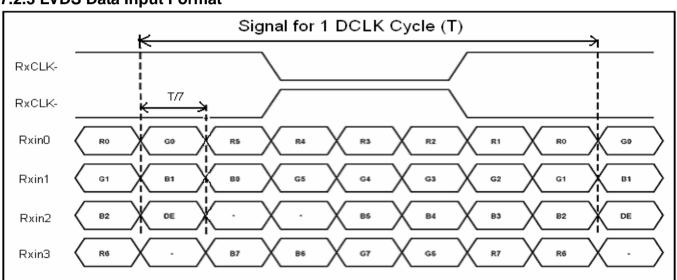
Mobile: +86-139-2528-0716

Web: www.kingtechgroup.cn





## 7.2.3 LVDS Data Input Format



# 8. Backlight Characteristic

5. Backlight Characteristic								
Item	Symbol	MIN	TYP	MAX	UNIT	Test Condition		
Supply Voltage	Vf	8.0	9.6	10.5	V	If=260mA		
Supply Current	If	-	260	-	mA	-		
Luminous Intensity for LCM	-	240	350		cd/m <sup>2</sup>	lf=260mA		
Uniformity for LCM	-	80		-	%	If=260mA		
Life Time	-	20000		-	Hr	If=260mA		
Backlight Color	White							

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25°C and IL=200mA

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original Brightness at Ta=25°C and IL=200mA. The LED life time could be decreased if operating IL is larger than 200mA

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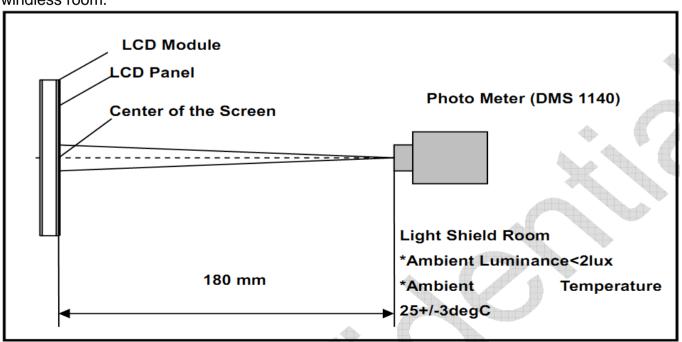


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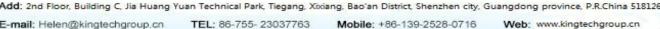
# 9. Optical Characteristics

Item	Conditions		Min.	Тур.	Max.	Unit	Note	
Viewing Angle (CR>10)	Horizontal	θL	75	85	-	degree		
		θR	75	85	-		(1),(2),(6)	
	Vertical	θт	75	85	-			
		θв	75	85	-			
Contrast Ratio	Center		600	800	-	-	(1),(3),(6)	
LCM Luminance	Center poi	nt	250	350	-	Cd/m <sup>2</sup>		
Doopongo Timo	Rising		-	10	20		(4) (4) (6)	
Response Time	Falling		-	15	30	ms	(1),(4),(6)	
	2.40 %			0.5778		-		
			Тур.	0.3362	Typ. +0.05	-	(1), (6)	
				0.3162		-		
CF Color				0.5920		-		
Chromaticity (CIE1931)				0.1495		-		
(0.2.00.)			-0.05	0.1120		-		
				0.2858		-		
	White y			0.3351		-		
Luminance uniformity	Yυ		75	80	-	%	(1)	

Note (1) Measurement Setup: The LCD module should be stabilized at given temp. 25°C for 15 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 15 minutes in a windless room.



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Note (2) Definition of Viewing Angle Normal line  $\theta = \Phi = 0^{\circ}$ Φ=90° 12 o'clock direction θι Φ=180°  $\Phi=0^{\circ}$ **Active Area** 

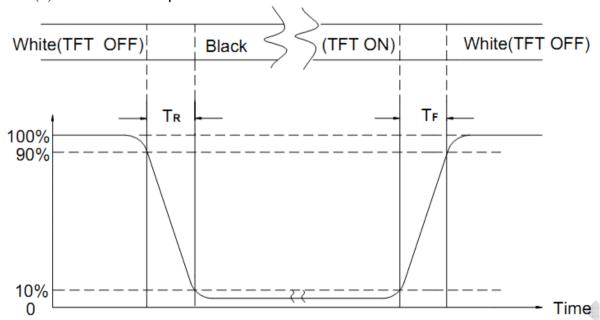
Note (3) Definition of Contrast Ratio (CR)

The contrast ratio can be calculated by the following expression Contrast Ratio (CR) = L63 / L0

 $\Phi = 270^{\circ}$ 

L63: Luminance of gray level 63, L0: Luminance of gray level 0

Note (4) Definition of response time



Note (5) Definition of Transmittance (Module is without signal input) Transmittance = Center Luminance of LCD / Center Luminance of Back Light x 100%

Note (6) Definition of color chromaticity (CIE1931)

Color coordinates measured at the center point of LCD

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10. Reliability Test Conditions and Methods

NO.	TEST ITEMS	TEST CONDITION	INSPECTION AFTER TEST	
1)	High Temperature Storage	70°C±2°C×120Hours		
2	Low Temperature Storage	-20°C±2°C×120Hours		
3	High Temperature Operating	70°C±2°C×120Hours	Inspection after 2~4hours	
4	Low Temperature Operating	-10°C±2°C×120Hours	storage at room temperature, the samples should be free from	
5	Temperature Cycle(Storage)	-20°C $\longrightarrow$ 25°C $\longrightarrow$ 70°C (30min) 1cycle Total 10cycle	defects: 1, Air bubble in the LCD. 2, Seal leak. 3, Non-display. 4, Missing segments.	
6	Damp Proof Test (Storage)	40°C±5°C×90%RH×120Hours	5, Glass crack. 6, Current IDD is twice	
7	Vibration Test	Random Vibration : ISTA-3A 1Hz~200Hz,Grms=0.53 Half hours for direction of Z.	higher than initial value. 7, The surface shall be free from damage. 8, The electric characteristic requirements	
8	Drooping Test	Drop to the ground from 1M height one time every side of carton. (packing condition test will be tested by a carton)	shall be satisfied.	
9	ESD Test	± 2KV, Human Body Mode, 100pF/1500Ω		

#### REMARK:

- 1. The Test samples should be applied to only one test item.
- 2, Sample side for each test item is 5~10pcs.
- 3, For Damp Proof Test, Pure water(Resistance  $> 10 \text{M}\Omega$ ) should be used.
- 4.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.
- 5, EL evaluation should be accepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.
- 6, Failure Judgment Criterion: Basic Specification Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.
- 7, After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.
- 8. Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

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# 11. Inspection Standard

#### 11.1. QUALITY:

THE QUALITY OF GOODS SUPPLIED TO PURCHASER SHALL COME UP TO THE FOLLOWING STANDARD.

#### 11.1.1. INSPECTIONTOOLS AND INSTRUMENTS

Vernier calipers, film scales, multimeter, magnifying eyepiece, ND5%, luminance meter and so on.

#### 11.1.2. THE METHOD OF PRESERVING GOODS

AFTER DELIVERY OF GOODS FROM KINGTECH TO PURCHASER. PURCHASER SHALL CONTROL THE LCM AT -10 TO 40, AND IT MIGHT BE DESIRABLE TO KEEP AT THE NORMAL ROOM TEMPERATURE AND HUMIDITY UNTIL INCOMING INSPECTION OR THROWING INTO PROCESS LINE.

#### 11.1.3. INCOMING INSPECTION

(A) THE METHOD OF INSPECTION

IF PURCHASER MAKE AN INCOMING INSPECTION, A SAMPLING PLAN SHALL BE APPLIED ON THE CONDITION THAT QUALITY OF ONE DELIVERY SHALL BE REGARDED AS ONE LOT.

(B) THE STANDARD OF QUALITY

ISO-2859-1 (SAME AS MIL-STD-105E), LEVEL: II

	, ,
CLASS	AQL(%)
CRITICAL	0.4 %
MAJOR	0.65 %
MINOR	1.5 %

EVERY ITEM SHALL BE INSPECTED ACCORDING TO THE CLASS.

(C) MEASURE

IF AS THE RESULT OF ABOVE RECEIVING INSPECTION, A LOT OUT IS DISCOVERED. PURCHASER SHALL BE INFORM SELLER OF IT WITHIN SEVEN DAYS. BUT FIRST SHIPMENT WITHIN FOURTEEN DAYS.

#### 11.1.4. WARRANTY POLICY

KINGTECH WILL PROVIDE ONE-YEAR WARRANTY FOR THE PRODUCTS ONLY IF UNDER SPECIFICATION OPERATING CONDITIONS. KINGTECH WILL REPLACE NEW PRODUCTS FOR THESE DEFECT PRODUCTS WHICH UNDER WARRANTY PERIOD AND BELONG TO THE RESPONSIBILITY OF KINGTECH

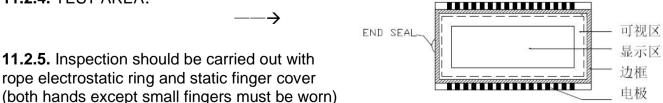
#### 11.2. CHECKING CONDITION

- **11.2.1.**CHECKING DIRECTION SHALL BE IN THE 45 DEGREE AREA TO FACE THE SAMPLE.
- 11.2.2.CHECKER SHALL SEE OVER 300±25 mm. WITH BARE EYES FAR FROM SAMPLE
- **11.2.3.** Ambient Illumination:

0 ~30 Lux for functional inspection

500 ~ 1200 Lux for external appearance inspection.

**11.2.4.** TEST AREA:



11.2.6. The inspector may make a visual inspection or a comparative examination with a film

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ruler and a magnifying eyepiece. Individual defects shall be determined according to the limited samples.

- **11.2.7.** Functional testing uses electrical testing fixtures or test fixtures required by customers.
- 11.2.8. the ion fan should be used when testing.

# 11.2.9. the principle of judgment

11.3.1 If the defect outside the visual area does not affect the assembly and display, it will be judged as a good product.

11.3.2 Poor definition

Pixel:

A combination of three sub-pixels (Red + Green + Blue).



#### Dot:

Any of the sub-pixels (Red or Green or Blue).







# **Bright and dark dots:**

A point pixel (sub-pixel: R, G, B pixels) is lit or turned off during the display function test. **Highlights**:

Usually considered to be shown on a black screen.

## Dark spots:

They are generally considered to be shown on R, G, B solid colors or white images.

### Neighborhood:

Two or three adjacent point pixels (dot: sub-pixel) connected together (R, G or G, B or B, R or RGB).

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## 11.3. INSPECTION PLAN:

11.0. 11101 20	HON PLAN :		
CLASS	ITEM	JUDGEMENT	CLASS
PACKING &	1. OUTSIDE AND INSIDE PACKAGE	"MODEL NO.", "LOT NO." AND "QUANTITY" SHOULD INDICATE ON THE PACKAGE.	Minor
INDICATE	2. MODEL MIXED AND QUANTITY	OTHER MODEL MIXEDREJECTED  QUANTITY SHORT OR OVERREJECTED	Critical
	3. PRODUCT INDICATION	"MODEL NO." SHOULD INDICATE ON THE PRODUCT	Major
	4. DIMENSION,	ACCORDING TO SPECIFICATION OR	
ASSEMBLY	LCD GLASS SCRATCH AND SCRIBE DEFECT.	DRAWING.	Major
	5. VIEWING AREA	POLARIZER EDGE OR LCD'S SEALING LINE IS VISABLE IN THE VIEWING AREAREJECTED	Minor
	6. BLEMISH - BLACK SPOT - WHITE SPOT IN THE LCD AND LCD GLASS CRACKS	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
APPEARANCE	7. BLEMISH - BLACK SPOT WHITE SPOT AND SCRATCH ON THE POLARIZER	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
	8. BUBBLE IN POLARIZER	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
	9. LCD'S RAINBOW COLOR	STRONG DEVIATION COLOR ( OR NEWTON RING) OF LCDREJECTED.  OR ACCORDING TO LIMITED SAMPLE ( IF NEEDED, AND INSIDE VIEWING AREA )	Minor
	10. ELECTRICAL AND OPTICAL CHARACTERISTICS (CONTRAST: VOP: CHROMATICITY ETC.)	ACCORDING TO SPECIFICATION OR DRAWING . (INSIDE VIEWING AREA)	Critical
ELECTRICAL	11.MISSING LINE	MISSING DOT. LINE . CHARACTERREJECTED	Critical
	12.SHORT CIRCUIT WRONG PATTERN DISPLAY	NO DISPLAY · WRONG PATTERN  DISPLAY · CURRENT CONSUMPTION  OUT OF SPECIFICATION REJECTED	Critical
	13. DOT DEFECT (FOR COLOR AND TFT)	ACCORDING TO STANDARD OF VISUAL INSPECTION	Minor

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NO.	CLASS	ITEM	JUDGEMENT			
			(A) ROUND TYPE: unit : mm.			
MINOR			DIAMETER (mm.)	ACCEPTABLE Q'TY		
			Φ ≤ 0.15	Distance>1mm		
		BLACK AND WHITE SPOT FOREIGN MATERIEL DUST IN THE CELL BLEMISH SCRATCH	0.15 < Φ ≤ 0.4	3 (Distance>15mm)		
			0.4 < Φ	0		
	MINOR		NOTE: Φ=(LENGTH+WIDTH	)/2		
11.4.1	MINTOIX		(B) LINEAR TYPE:	unit: mm.		
			LENGTH WIDTH	ACCEPTABLE Q'TY		
		34444	W	≦0.03 Distance≥1mm		
			L ≦ 4.0 0.03 < W	≦0.05 3 (Distance>15mm)		
			0.05 < W	FOLLOW ROUND TYPE		
<u> </u>		2	00	unit : mm.		
			DIAMETER	ACCEPTABLE Q'TY		
	MINOR	BUBBLE IN POLARIZER DENT ON POLARIZER	Φ ≤ 0.2	Distance≥1mm		
11.4.2			0.2 < Φ ≤ 0.5	3 (Distance>15mm)		
			0.5 < Ф	0		
			in to	ā.		
		Dot Defect	The second secon	1 22 22 7		
			Items	ACC. Q'TY		
			Bright dot	N≦2 (Distance≥15mm)		
			Dark dot	N≦3 (Distance≥15mm)		
			Pixel Define : Pixe	el —		
			RG	B		
11.4.3	MINOR		◆ Dot → ◆ Do	t Dot		
330034423			Note 1: The definition of dot: The			
			CONTROL OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE	rded as one defective dot.		
			30	visible by 5% ND filter N≤5		
			Note 2: Bright dot: Dots appear	The state of the s		
				isplaying under black pattern.		
			Note 3: Dark dot: Dots appear d			
			which LCD panel is displaying under pure red, green			
			,blue pattern.	THE RESERVE TO SERVE THE PROPERTY OF THE PROPE		
	MINOR	2000	Not visible thriugh 5% ND fill	ter in 50% gray or judge		
11,4,4		Mura	by limit sample if necessary			
1 N		3 20	by minic sample in necessary	W.		

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NO.	CLASS	ITEM	JUDGEMEN	Т
11.4.4	MINOR	LCD GLASS CHIPPING	S	Y > S Reject
11.4.5	MINOR	LCD GLASS CHIPPING	SY	X or Y > S Reject
11.4.6	MAJOR	LCD GLASS GLASS CRACK	Y	Y > (1/2) T Reject
11.4.7	MAJOR	LCD GLASS SCRIBE DEFECT	A + B	1. a> L/3 , A>1.5mm. Reject  2. B: ACCORDING TO DIMENSION
11.4.8	MINOR	LCD GLASS CHIPPING ( ON THE TERMINAL AREA )	T	$\Phi = (x+y)/2 > 2.5 \text{ mm}$ Reject
11.4.9	MINOR	LCD GLASS CHIPPING ( ON THE TERMINAL SURFACE )	T Z X	Y > (1/3) T Reject
11.4.10	MINOR	LCD GLASS CHIPPING	T Z	Y > T Reject

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# 12. Handling Precautions

## 12.1 Mounting method

The LCD panel of KingtechTFT module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

### 12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (CI), Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

#### 12.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to Power or Ground, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

#### 12.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

#### 12.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

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Usage under the maximum operating temperature, 50%Rh or less is required.

#### 12.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else. [It is recommended to store them as they have been contained in the inner container at the time of delivery from us

#### 12.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands. please wash it off well with soap and water

## 13. Precaution for Use

#### 13.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

#### 13.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification
- When a new problem is arisen which is not specified in this specifications
- When an inspection specifications change or operating condition change in customer is reported to Kingtech TFT, and some problem is arisen in this specification due to the change
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

# 14. Packing Method

**TBD** 

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