





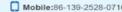
# **PRODUCT SPECIFICATIONS**

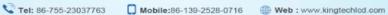
For Customer:			☐ : APPROVAL FOR SPECIFICATION		
Customer Model No			_ : APPROVAL FOR SAMPLE		
Modu	ule No.: PV08415D0120Q		Date : 2023.8.10		
of Con	tents				
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# 2. Revision Record

Date	Rev.No.	Page	Revision Items	Prepared
2023.8.10	V0		The first release	YZJ











# 3. General Specifications

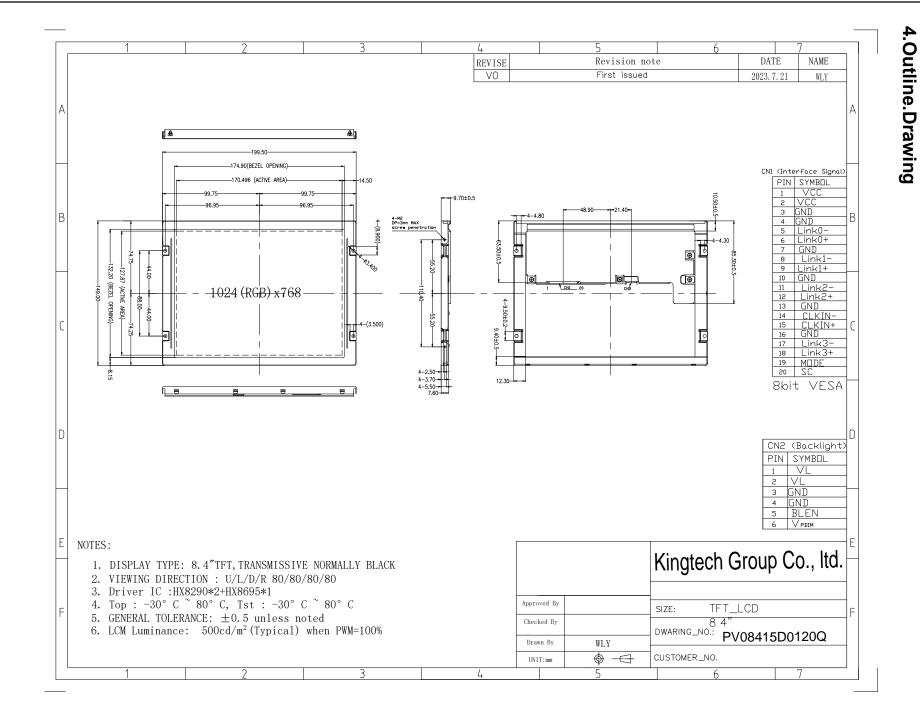
PV08415D0120Q is a TFT-LCD module. It is composed of a TFT-LCD panel, driver IC, FPC,a back light unit. The 8.4 " display area contains 1024x768 pixels and can display up to 16.7M colors. This product accords with RoHS environmental criterion.

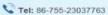
Item	Contents	Unit	Note
LCD Type	TFT	-	
Display color	16.7M		
Viewing Direction	ALL	O'Clock	
Operating temperature	-30~+80	$^{\circ}$ C	
Storage temperature	-30~+80	$^{\circ}$ C	
Module size	199.50X149.00X9.7	mm	
Active Area(W×H)	170.496X127.872	mm	
Number of Dots	1024x768	dots	
Driver IC	HX8290*2+HX8695	-	
Power Supply Voltage	3.3	V	
Outline Dimensions	Refer to outline drawing	-	
Backlight	7S4P-LEDs (white)	pcs	
Interface	8bits - LVDS	-	

Mobile:86















# 5. Absolute Maximum Ratings(Ta=25°C)

#### 5.1 Electrical Absolute Maximum Ratings.(Vss=0V, Ta=25°C)

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	$V_{DD}$	-0.3	4.0	V	1, 2

#### Notes:

- 1. If the module is above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.
- 2.  $V_{DD} > V_{SS}$  must be maintained.
- 3. Please be sure users are grounded when handing LCD Module.

#### 5.2 Environmental Absolute Maximum Ratings.

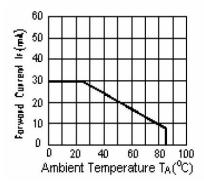
Item	Stor	rage	Operating		
Rom	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	-30°C	80°C	-30°C	80°C	
Humidity	-	-	-	-	

- 1. The response time will become lower when operated at low temperature.
- 2. Background color changes slightly depending on ambient temperature.

The phenomenon is reversible.

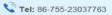
3. Ta<=40 °C:85%RH MAX.

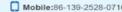
Ta>=40 °C:Absolute humidity must be lower than the humidity of 85%RH at 40 °C.



**ILED VS TEMP** 











## 6. Electrical Specifications and Instruction Code

### 6.1 Electrical characteristics(Vss=0V, Ta=25 ℃)

Parameter		Symbol	Condition	Min	Тур	Max	Unit	Note
Power su	pply	VDD	Ta=25°C	3.0	3.3	3.6	V	
Input 'H' voltage 'L'	'H'	V <sub>IH</sub>	V <sub>DD</sub> =3.3V	0.7V <sub>DD</sub>	-	$V_{DD}$	V	
	'L'	VIL	V <sub>DD</sub> =3.3V	0	-	0.3V <sub>DD</sub>	V	

Note: If one of the above items is exceeded its maximum limitation momentarily, the quality of the product may be degraded. Absolute maximum limitation, therefore, specify the values exceeding which the product may be physically damaged. Be sure to use the product within the recommend range.

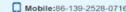
### 6.2 LED backlight specification(VSS=0V ,Ta=25 $\mathcal{C}$ )

Item	Symbol	Condition	Min	Тур	Max	Unit	Note
Supply voltage VLED	V <sub>f</sub>	If=240mA	23.2	24.8	26.4	V	
Uniformity	∆Вр	If=240mA	80	85	-	%	
LED Life Time	-	-	20000	-	-	hr	1

Note 1: Brightness to be decreased to 50% of the initial value at ambient temperature TA=25  $\mathcal C$ 











### 6.3 Interface signals **6.3.1 LCM PIN**

Pin No.	Symbol	I/O	Function					
1-2	VCC	Р	Power supply					
3-4	GND	Р	Ground.					
5	LINK0-	I	LVDS long0 input					
6	LINK0+	I	LVDS lane0 input					
7	GND	Р	Ground.					
8	LINK1-	I	LVDS long1 input					
9	LINK1+	I	LVDS lane1 input					
10	GND	Р	Ground.					
11	LINK2-	I	LVDS lang2 input					
12	LINK2+	I	LVDS lane2 input					
13	GND	Р	Ground.					
14	CLKIN-	ı	LVDS CLK input					
15	CLKIN+	I	EVDS CER IIIput					
16	GND	Р	Ground.					
17	LINK3-	I	LVDS long2 input					
18	LINK3+	I	LVDS lane3 input					
19	MODE	-	Not connection					
20	SC	I	Scan direction control (Low=Normal, High=Reverse)					



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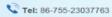


### 6.3.2 CN 2(Backlight)

Backlight-side connector: FI-S6P-HFE (JAE)

Corresponding connector: FI-S6S (JAE)

Pin No.	Symbol	I/O	Function
1-2	VL	Р	Power supply For BL.
3-4	GND	Р	Ground.
5	BLEN	ı	LED driver enable input
6	VPDIM	I	PWM dimming control input.



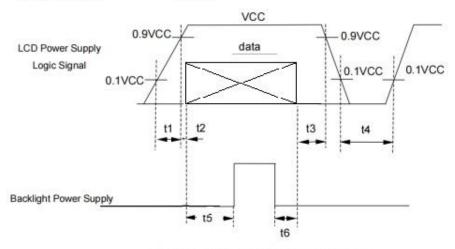






#### 6.4 Power and signals sequence

 $0.1~ms \leq t1 \leq 10~ms$ 200 ms ≤ t4  $0 \le t2 \le 50 \text{ ms}$  $200 \text{ ms} \leq t5$  $0 < t3 \le 50 \text{ ms}$  $0 \le t6$ 

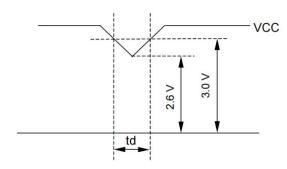


data: RGB DATA, DCLK, DENA, MODE, SC

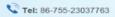
VCC-dip conditions:

- 1) When  $2.6 \text{ V} \leq \text{VCC} \leq 3.0 \text{ V}$ ,  $td \leq 10 \text{ ms}$
- 2) When VCC < 2.6 V

VCC-dip conditions should also follow the power and signals sequence.











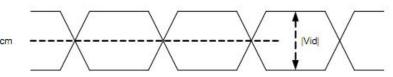


#### **6.5 AC Characteristics**

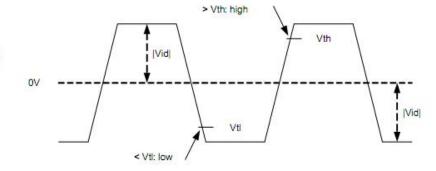
#### 6.5.1 For the digital circuit: LVDS mode

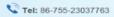
Damani atau	Comples	Condition		11			
Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit	
Differential input high Threshold voltage	Vth	Vcm=1.2V	-	-	+0.1	V	
Differential input low threshold voltage	VtI	¥	-0.1	<b>-</b> 1	-	٧	
Differential input common Mode voltage	V <sub>CM</sub>		1	1.2	1.7- V <sub>id</sub>  /2	٧	
LVDS input voltage	V <sub>INLV</sub>		0.7		1.7	V	
Differential input voltage	Vid	-	0.1	12	0.6	V	
Differential input leakage Current	Ilvleak		-10	-	+10	μΑ	

Single-ended: LVCLKP(R), LVCLKN(R), LVD[3:0]P(R), LVD [3:0]N(R)



Differential: LVCLKP(R)-LVCLKN(R), LVD [3:0]P(R)-LVD [3:0]N(R)







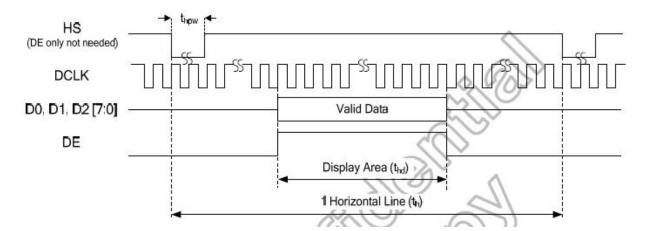




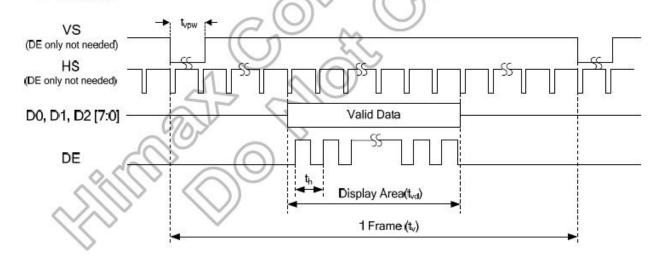
#### 6.5.2 Input timing table

Parameter	Symbol	Spec.			Unit
		Min.	Тур.	Max.	
DCLK frequency	Fdclk	49	50	73.6	MHz
Horizontal valid data	thd		1024		DCLK
Hsync pulse width	thpw	10	12	255	DCLK
Hsync back porch	thbp	5	16	255	DCLK
Hsync front porch	thfp	24	26	260	DCLK
1 Horizontal line	th	1053	1066	1331	DCLK
Vertical valid data	tvd		768		Н
Vsync pulse width	tvpw	1	3	20	Н
Vsync back porch	tvbp	2	5	255	Н
Vsync front porch	tvfp	5	8	260	Н
1 Vertical field	tv	775	781	921	Н
Frame rate	FR		60		Hz

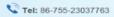
#### Horizontal

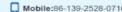


#### Vertical



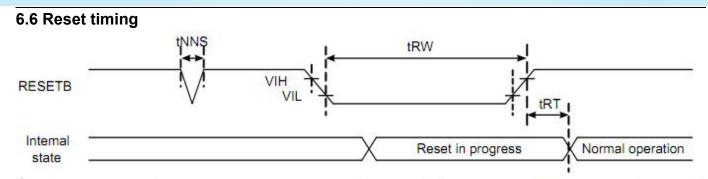






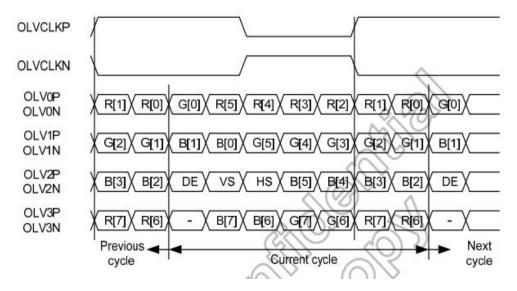






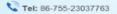
Cianal	Davamatas	Cumbal		Unit		
Signal	Parameter	Symbol	Min.	Тур.	Max.	Unit
	Reset pulse width	tRW	10		-	μs
RESETB	Reset complete time	tRT	-	-	5	μs
	Negative spike noise width	tNNS		2	100	ns

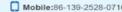
#### LVDS interface data format



8bit mode











# 7. Optical Characteristics

Item	Symbol		Condition	Min.	Тур.	Max.	Unit	Note
Brightness	Вр		<i>θ</i> =0° Φ=0°	-	500	-	Cd/m <sup>2</sup>	1
Uniformity	⊿Bp			75	8/0	-	%	1,2
Viewing Angle	3:00		Cr≥10	75	80	-	Deg	3
	6:00			75	80	-		
	9:00			75	80	-		
	12:00			75	80	-		
Contrast Ratio	Cr		<i>θ</i> =0° Φ=0°	800	1000	-	-	4
Response Time	$T_{r+}T_{f}$			-	22	25	ms	5
Color of CIE Coordinate	W	х	<i>θ</i> =0° Φ=0°	Typ-0 .05	TBD	Typ+0. 05	-	1,6
		у					-	
	R	х					-	
		у					-	
	G	х					-	
		у					-	
	В	х					-	
		у					-	
NTSC Ratio	S			-	70	-	%	

Note: The parameter is slightly changed by temperature, driving voltage and materiel

Note 1: The data are measured after LEDs are turned on for 5 minutes. LCM displays full white. The brightness is the average value of 9 measured spots. Measurement equipment BM-7 (Φ5mm) Measuring condition:

- Measuring surroundings: Dark room.
- Measuring temperature: Ta=25  $^{\circ}$ C.
- Adjust operating voltage to get optimum contrast at the center of the display.

Measured value at the center point of LCD panel after more than 5 minutes while backlight



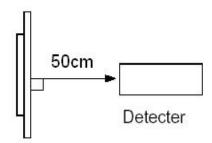
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turning on.

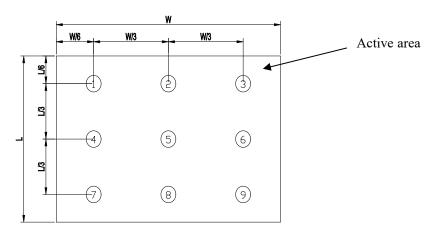


Note 2: The luminance uniformity is calculated by using following formula.

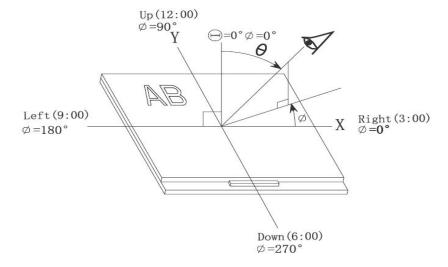
$$\triangle Bp = Bp (Min.) / Bp (Max.) \times 100 (%)$$

Bp (Max.) = Maximum brightness in 9 measured spots

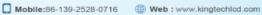
Bp (Min.) = Minimum brightness in 9 measured spots.



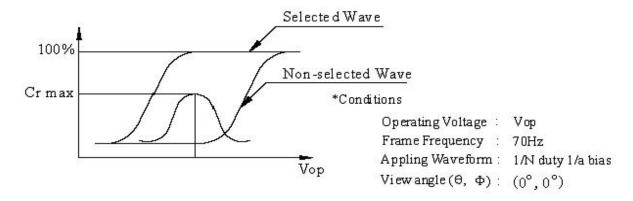
Note 3: The definition of viewing angle: Refer to the graph below marked by  $\vartheta$  and  $\Phi$ 



Note 4: Definition of contrast ratio.( Test LCD using DMS501)



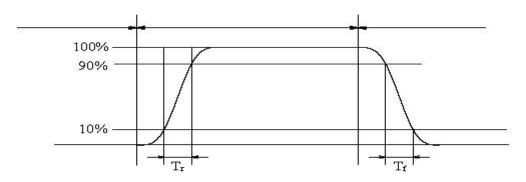




$$Contrast \ ratio(Cr) = \frac{Brightness \ of \ selected \ dots}{Brightness \ of \ non-selected \ dots}$$

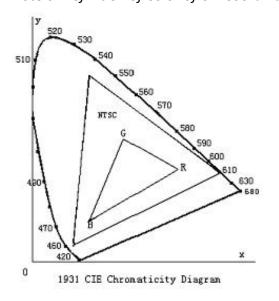
#### Note 5: Definition of Response time. (Test LCD using DMS501):

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes.Refer to figure as below.



The definition of response time

Note 6: Definition of Color of CIE Coordinate and NTSC Ratio.

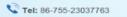


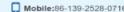
Note 7: Definition of cross talk.

Color gamut:

$$S = \frac{area \ of \ RGB \ triangle}{area \ of \ NTSC \ triangle} \times 100\%$$



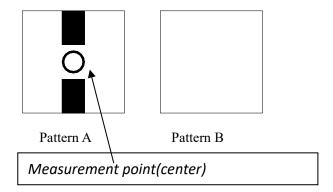




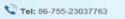


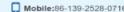


Cross talk ratio(%)=|pattern A Brightness-pattern B Brightness|/pattern A Brightness\*100



Electric volume value=3F+/-3Hex









# 8. Reliability Test Items and Criteria

No	Test Item	Test condition	Criterion		
1	High Temperature Storage	80°C±2°C 96H Restore 2H at 25°C Power off			
2	Low Temperature Storage	-30°C±2°C 96H Restore 2H at 25°C Power off	1. After testing,		
3	High Temperature Operation	70°C±2°C 96H Restore 2H at 25°C Power on	cosmetic and electrical defects should not happen.		
4	Low Temperature Operation	-20°C±2°C 96H Restore 4H at 25°C Power on	2. Total current consumption should not be more than twice		
5	High Temperature/Humidity Operation	60°C±2°C 90%RH 96H Power on	of initial value.		
6	Temperature Cycle	-30°C←			

Note: Operation: Supply 3.3V for logic system.

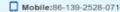
The inspection terms after reliability test, as below

-	-	
ITEM	Inspection	
Contrast	CR>50%	
IDD	IDD<200%	
Brightness	Brightness>60%	
Color Tone	Color Tone+/-0,05	





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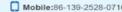
#### 9. Precautions for Use of LCD Modules

#### 9.1 Handling Precautions

- 9.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 9.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 9.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 9.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 9.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
  - Isopropyl alcohol — Ethyl alcohol Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:
  - Water - Ketone Aromatic solvents
- 9.1.6 Do not attempt to disassemble the LCD Module.
- 9.1.7 If the logic circuit power is off, do not apply the input signals.
- 9.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - a. Be sure to ground the body when handling the LCD Modules.
  - b. Tools required for assembly, such as soldering irons, must be properly ground.
  - c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
  - d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.



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#### 9.2 Storage precautions

- 9.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 9.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

0° $C\sim40$ °CTemperature:

Relatively humidity: ≤80%

- 9.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- 9.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

**END**